TOSHIBA

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74VHC245F, TC74VHC245FT, TC74VHC245FK

Octal Bus Transceiver

The TC74VHC245 is an advanced high speed CMOS OCTAL BUS TRANSCEIVER fabricated with silicon gate C^2MOS technology.

It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

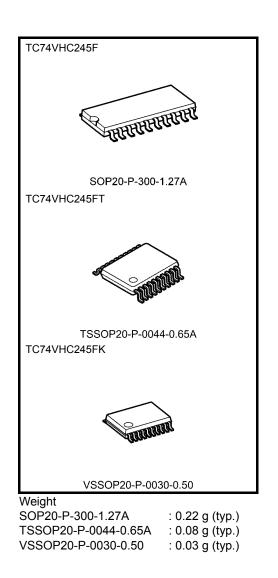
It is intended for two-way asynchronous communication between data busses. The direction of data transmission is determined by the level of the DIR input.

The enable input (\overline{G}) can be used to disable the device so that the busses are effectively isolated.

All inputs are equipped with protection circuits against static discharge.

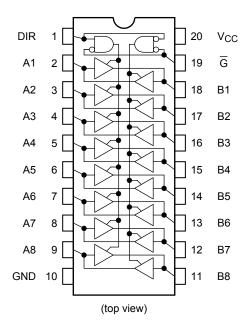
Features (Note 1) (Note 2) (Note 3)

- High speed: $t_{pd} = 4.0$ ns (typ.) at $V_{CC} = 5$ V
- Low power dissipation: $I_{CC} = 4 \mu A (max)$ at $Ta = 25^{\circ}C$
- High noise immunity: V_{NIH} = V_{NIL} = 28% V_{CC} (min)
- Balanced propagation delays: $t_{pLH} \simeq t_{pHL}$
- Wide operating voltage range: VCC (opr) = 2 V to 5.5 V
- Low noise: VOLP = 1.0 V (max)
- Pin and function compatible with 74ALS245
 - Note 1: Do not apply a signal to any bus terminal when it is in the output mode. Damage may result.
 - Note 2: All floating (high impedance) bus terminals must have their input levels fixed by means of pull up or pull down resistors.
 - Note 3: A parasitic diode is formed between the bus and V_{CC} terminals. Therefore bus terminal can not be used to interface 5 V to 3 V systems directly.

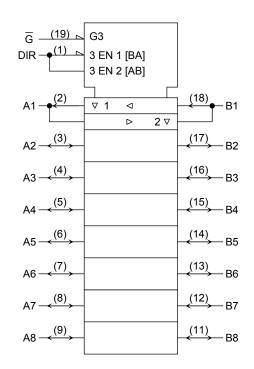


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Pin Assignment



IEC Logic Symbol



Truth Table

Inputs		Fund	Output		
G	DIR	A Bus	Output		
L	L	Output	Input	A = B	
L	Н	Input	B = A		
Н	Х	2	Z		

X: Don't care

Z: High impedance

Absolute Maximum Ratings (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V _{CC}	-0.5 to 7.0	V
DC input voltage (DIR, G)	VIN	-0.5 to 7.0	V
DC bus I/O voltage	V _{I/O}	-0.5 to V _{CC} + 0.5	V
Input diode current	IIК	-20	mA
Output diode current	lok	±20	mA
DC output current	lout	±25	mA
DC V _{CC} /ground current	ICC	±75	mA
Power dissipation	PD	180	mW
Storage temperature	T _{stg}	-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Operating Ranges (Note)

Characteristics	Symbol	Rating	Unit	
Supply voltage	V _{CC}	2.0 to 5.5	V	
Input voltage (DIR, G)	V _{IN}	0 to 5.5	V	
Bus I/O voltage	V _{I/O}	0 to V _{CC}	V	
Operating temperature	T _{opr}	-40 to 85	°C	
Input rise and fall time	dt/dv	0 to 100 (V _{CC} = 3.3 ± 0.3 V)	ns/V	
	uvuv	0 to 20 (V _{CC} = 5 \pm 0.5 V)	115/ V	

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs and bus inputs must be tied to either V_{CC} or GND. Please connect both bus inputs and the bus outputs with V_{CC} or GND when the I/O of the bus terminal changes by the function. In this case, please note that the output is not short-circuited.

Electrical Characteristics

DC Characteristics

Characteristics	Symbol	Test Condition			Ţ	Ta = 25°C			Ta = −40 to 85°C	
				$V_{CC}(V)$	Min	Тур.	Мах	Min	Max	
High-level input		_		2.0	1.50	-		1.50	_	v
voltage	VIH			3.0 to 5.5	V _{CC} × 0.7	-	_	V _{CC} × 0.7	_	
Low-level input				2.0	_		0.50		0.50	
voltage			—	3.0 to 5.5	_	_	V _{CC} × 0.3	—	V _{CC} × 0.3	V
			I _{OH} = -50 μA	2.0	1.9	2.0	_	1.9	_	
	V _{OH}	V _{IN} = V _{IH} or V _{IL}		3.0	2.9	3.0	—	2.9	—	V
High-level output voltage				4.5	4.4	4.5	—	4.4	—	
Ũ			I _{OH} = −4 mA	3.0	2.58		_	2.48	—	
			I _{OH} = −8 mA	4.5	3.94	-	—	3.80	—	
	V _{OL}	V _{IN} = V _{IH} or V _{IL}	I _{OL} = 50 μA	2.0	—	0.0	0.1		0.1	
				3.0	—	0.0	0.1	—	0.1	
Low-level output voltage				4.5	—	0.0	0.1		0.1	V
Ū.			I _{OL} = 4 mA	3.0	—	—	0.36	—	0.44	
			I _{OL} = 8 mA	4.5	—		0.36		0.44	
3-state output off-state current	I _{OZ}	V _{IN} = V _{IH} or V _{IL} V _{OUT} = V _{CC} or GND		5.5	_	_	±0.25		±2.50	μA
Input leakage current	I _{IN}	V _{IN} = 5.5 V or GND		0 to 5.5	_	_	±0.1	_	±1.0	μA
Quiescent supply current	Icc	V _{IN} = V _{CC} or GND		5.5	_	_	4.0	_	40.0	μA

AC Characteristics (input: $t_r = t_f = 3 \text{ ns}$)

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = −40 to 85°C		Unit	
	- ,		V _{CC} (V)	C _L (pF)	Min	Тур.	Max	Min	Max	
		_	3.3 ± 0.3	15	_	5.8	8.4	1.0	10.0	
Propagation delay	t _{pLH}		5.5 ± 0.5	50	_	8.3	11.9	1.0	13.5	ns
time	t _{pHL}		FO . O F	15	_	4.0	5.5	1.0	6.5	
			5.0 ± 0.5	50	_	5.5	7.5	1.0	8.5	
		R _L = 1 kΩ	3.3 ± 0.3	15	_	8.5	13.2	1.0	15.5	- ns
3-state output enable	t _{pZL} t _{pZH}			50	_	11.0	16.7	1.0	19.0	
time			5.0 ± 0.5	15	_	5.8	8.5	1.0	10.0	
				50	_	7.3	10.6	1.0	12.0	
3-state output disable	t _{pLZ}	R _L = 1 kΩ	3.3 ± 0.3	50	_	11.5	15.8	1.0	18.0	ns
time	t _{pHZ}		5.0 ± 0.5	50	_	7.0	9.7	1.0	11.0	115
	t _{osLH}		3.3 ± 0.3	50	_	_	1.5	_	1.5	
Output to output skew	t _{osHL}	(Note 1)	5.0 ± 0.5	50	_	_	1.0	_	1.0	ns
Input capacitance	CIN	DIR, G			_	4	10	_	10	pF
Bus input capacitance	C _{I/O}	An, Bn			_	8	_	_	_	pF
Power dissipation capacitance	C _{PD}			(Note 2)	_	21	_	_	_	pF

Note 1: Parameter guaranteed by design.

 $t_{OSLH} = |t_{pLHm} - t_{pLHn}|, t_{OSHL} = |t_{pHLm} - t_{pHLn}|$

Note 2: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

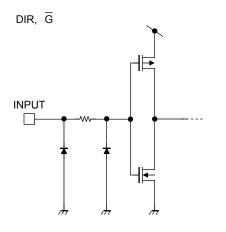
Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC} / 8 (per bit)$

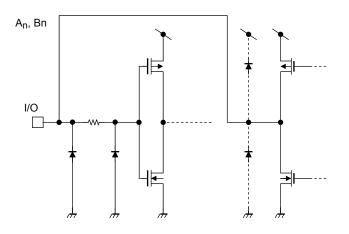
Noise Characteristics (input: t_r = t_f = 3 ns) (Note)

Characteristics	Symbol	Test Condition		Ta = 25°C		Unit
Characteristics	Symbol		V _{CC} (V)	Тур.	Max	Offic
Quiet output maximum dynamic	Maria		5.0	0.7	1.0	V
V _{OL}	VOLP	C _L = 50 pF	5.0	0.7	1.0	v
Quiet output minimum dynamic	Varia	C ₁ = 50 pF	5.0	-0.7	-1.0	V
V _{OL}	V _{OLV}	С <u>Г</u> – 50 рі	5.0	-0.7	-1.0	v
Minimum high level dynamic input voltage	V _{IHD}	C _L = 50 pF	5.0		3.5	V
Maximum low level dynamic input voltage	V _{ILD}	C _L = 50 pF	5.0		1.5	V

Input Equivalent Circuit



Bus Terminal Equivalent Circuit

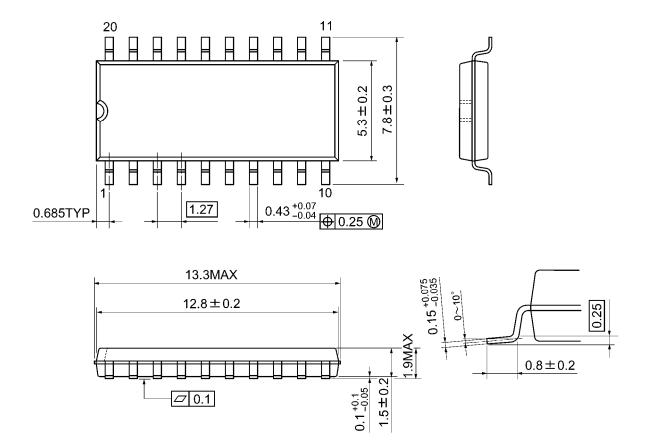




Package Dimensions

SOP20-P-300-1.27A

Unit: mm



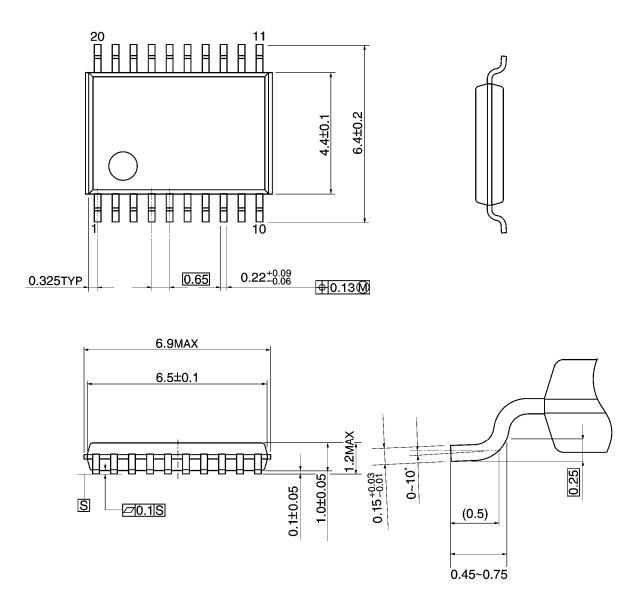
Weight: 0.22 g (typ.)

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Package Dimensions

TSSOP20-P-0044-0.65A

Unit: mm



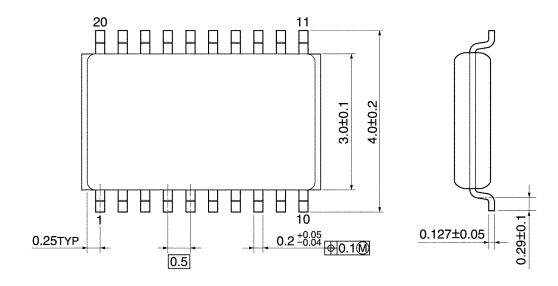
Weight: 0.08 g (typ.)

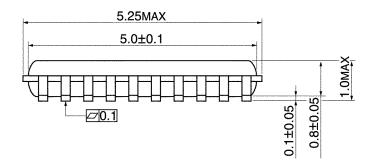


Package Dimensions

VSSOP20-P-0030-0.50

Unit: mm





Weight: 0.03 g (typ.)

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